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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Confirmation No.: 9593

NAKAMURA

Art Unit: 2188

Application No.: 09/922,742

Examiner: P. Vital

Filed: August 7, 2001

Docket No.: 108066-00038

For: CLOCK SYNCHRONIZED DYNAMIC MEMORY AND CLOCK
SYNCHRONIZED INTEGRATED CIRCUIT

REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.121

Assistant Commissioner For Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

December 9, 2004

Sir:

Responsive to the Office Action dated September 9, 2004, consideration of the following remarks is respectfully requested.